

**MAXIM****CMOS Analog Switches****DG304(A)/DG305(A)/DG306(A)/DG307(A)****General Description**

Maxim's DG304-DG307 and DG304A-DG307A CMOS dual and quad analog switches combine low power operation with fast switching times and superior DC and AC switch characteristics. On resistance is less than 50Ω and is essentially constant over the analog signal range. Device specifications are ideal for battery powered circuitry.

These switches are available in a variety of formats as outlined below in the Pin Configurations section. The switch control logic inputs are compatible with CMOS logic. Also featured are "break-before-make" switching and low charge injection.

Maxim's DG304-DG307 and DG304A-DG307A families are electrically compatible and pin compatible with the original manufacturer's devices. All devices will operate with power supplies ranging from  $\pm 5V$  to  $\pm 18V$ . Single supply operation is implemented by connecting  $V^-$  to GND.

**Applications**

Portable Instruments  
Low Power Sample/Holds  
Power Supply Switching  
Programmable Gain Amplifiers  
SPDT and DPDT Functions  
Process Control and Telemetry

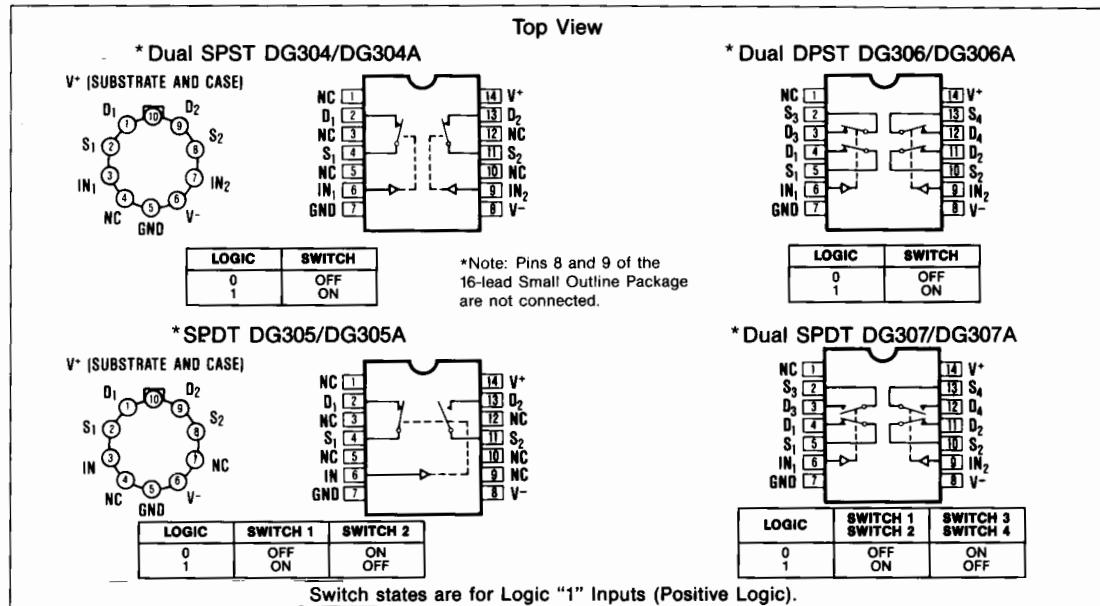
**Features**

- ◆ Monolithic Low Power CMOS
- ◆ Latch-Up Proof Construction
- ◆ Fully Compatible 2nd Source
- ◆ Low On Resistance,  $<50\Omega$
- ◆ Fast Switching Time
- ◆  $V^+$  to  $V^-$  Analog Signal Range
- ◆ Single Supply Capability

**Ordering Information**

PART	TEMP. RANGE	PACKAGE
DG304C/D	0°C to +70°C	Dice
DG304CJ	0°C to +70°C	14 Lead Plastic DIP
DG304CWE	0°C to +70°C	16 Lead Wide SO
DG304CK	0°C to +70°C	14 Lead CERDIP
DG304BWE	-25°C to +85°C	16 Lead Wide SO
DG304BK	-25°C to +85°C	14 Lead CERDIP
DG304BA	-25°C to +85°C	10 Lead Metal Can
DG304AK	-55°C to +125°C	14 Lead CERDIP
DG304AA	-55°C to +125°C	10 Lead Metal Can

*Ordering Information continued at end of data sheet.*

**Pin Configurations****MAXIM**

Maxim Integrated Products 1

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at 1-888-629-4642, or visit Maxim's website at [www.maxim-ic.com](http://www.maxim-ic.com).

## CMOS Analog Switches

### ABSOLUTE MAXIMUM RATINGS

Voltages Referenced to V <sup>-</sup>	
V <sup>+</sup> (DG304-DG307) .....	36V
V <sup>+</sup> (DG304A-DG307A) .....	44V
GND .....	25V
Digital Inputs, V <sub>S</sub> , V <sub>D</sub> (Note 1) .....	-4V to (V <sup>+</sup> + 4V) or 30mA, whichever occurs first.
Current, Any Terminal Except S or D .....	30mA
Continuous Current, S or D .....	30mA (Pulsed at 1msec, 10% duty cycle max) .....
Storage Temperature (A & B Suffix) .....	-65°C to 150°C
(C Suffix) .....	-65°C to 125°C

Operating Temperature (A Suffix) ..... -55°C to 125°C  
(B Suffix) ..... -25°C to 85°C  
(C Suffix) ..... 0°C to 70°C

Lead Temperature (Soldering 10 sec.) ..... +300°C

#### Power Dissipation\*

Cerdip (K) (Derate 11mW/°C above 75°C) .....	825mW
Plastic DIP (J) (Derate 6.5mW/°C above 25°C) ..	470mW
Metal Can (A) (Derate 6mW/°C above 75°C) ..	450mW

\* Device mounted with all leads soldered or welded to PC board.

Stresses listed under "Absolute Maximum Ratings" may be applied (one at a time) to devices without resulting in permanent damage. These are stress ratings only, and functional operation of the device at these or any other conditions above those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum ratings conditions for extended periods may affect device reliability.

### ELECTRICAL CHARACTERISTICS (V<sup>+</sup> = +15V, V<sup>-</sup> = -15V, GND = 0V, T<sub>A</sub> = 25°C, unless otherwise indicated)

	PARAMETER	SYMBOL	TEST CONDITIONS	DG304-DG307A DG304A-DG307AA			DG304-DG307B/C DG304A-DG307AB/C			UNITS
				MIN	TYP	MAX	MIN	TYP	MAX	
	Analog Signal Range	V <sub>ANALOG</sub>	I <sub>S</sub> = 10mA, V <sub>in</sub> = 3.5V or 11.0V	-15	15	-15	15	15	15	V
SWITCH	Drain-Source ON Resistance	r <sub>DS(on)</sub>	V <sub>in</sub> = 3.5V or V <sub>in</sub> = 11.0V	I <sub>S</sub> = -10mA, V <sub>D</sub> = 10V	30	50	30	50	50	Ω
	Source OFF Leakage Current	I <sub>S(off)</sub>		I <sub>S</sub> = 10mA, V <sub>D</sub> = -10V	30	50	30	50	50	
	Drain OFF Leakage Current	I <sub>D(off)</sub>		V <sub>S</sub> = 14V, V <sub>D</sub> = -14V	0.1	1	0.1	5	5	nA
	Drain ON Leakage Current	I <sub>D(on)</sub>		V <sub>S</sub> = -14V, V <sub>D</sub> = 14V	-1	-0.1	-5	-0.1	-0.1	
	Input Current/Voltage High	I <sub>INH</sub>		V <sub>in</sub> = 5.0V	-1	-0.001	-1	-0.001	-0.001	μA
INPUT	Input Current/Voltage Low	I <sub>INL</sub>	V <sub>in</sub> = 0V	V <sub>in</sub> = 15V	0.001	1	0.001	1	1	
	Turn-ON Time	t <sub>on</sub>		See Switching Time Test Circuit		110	250	110	250	ns
DYNAMIC	Turn-OFF Time	t <sub>off</sub>			70	150	70	150		
	Break-Before-Make Interval	t <sub>on</sub> -t <sub>off</sub>	f = 1MHz, V <sub>in</sub> = 3.5V or V <sub>in</sub> = 11.0V	See Break-Before-Make Time Test Circuit DG305(A)/DG307(A) Only		50		50		
	Charge Injection	Q		C <sub>L</sub> = 10nF, R <sub>gen</sub> = 0Ω, V <sub>gen</sub> = 0V	12		12		pC	
	Source OFF Capacitance	C <sub>S(off)</sub>		V <sub>S</sub> = 0V	14		14			
	Drain OFF Capacitance	C <sub>D(off)</sub>		V <sub>D</sub> = 0V	14		14			
	Channel-ON Capacitance	C <sub>D(on)</sub> + C <sub>S(on)</sub>		V <sub>S</sub> = V <sub>D</sub> = 0V	40		40			pF
	Input Capacitance	C <sub>in</sub>	f = 1MHz	V <sub>in</sub> = 0V	6		6			
				V <sub>in</sub> = 15V	7		7			
	Off Isolation (Note 4)		V <sub>in</sub> = 0V, R <sub>L</sub> = 1kΩ V <sub>S</sub> = 1 V <sub>RMS</sub> , f = 500kHz		62		62			
	Crosstalk (Channel to Channel)				74		74			

(See Notes next page).

## CMOS Analog Switches

**DG304(A)/DG305(A)/DG306(A)/DG307(A)**

### ELECTRICAL CHARACTERISTICS (Continued)

( $V^+ = +15V$ ,  $V^- = -15V$ ,  $GND = 0V$ ,  $T_A = 25^\circ C$ , unless otherwise indicated)

	PARAMETER	SYMBOL	TEST CONDITIONS	DG304-DG307A DG304A-DG307AA			DG304-DG307B/C DG304A-DG307AB/C			UNITS
				MIN	TYP	MAX	MIN	TYP	MAX	
SUPPLY	Positive Supply Current	$I^+$	$V_{in} = 15.0V$ (All Inputs)	0.001	10	0.001	10	0.001	10	$\mu A$
	Negative Supply Current	$I^-$		-10	-0.001	-10	-0.001	-10	-0.001	
	Positive Supply Current	$I^+$	$V_{in} = 0V$ (All Inputs)	0.001	10	0.001	10	0.001	10	
	Negative Supply Current	$I^-$		-10	-0.001	-10	-0.001	-10	-0.001	

### ELECTRICAL CHARACTERISTICS (Over Temperature)

( $V^+ = +15V$ ,  $GND = 0V$ ,  $T_A = \text{Over Temperature Range, unless otherwise noted}$ )

	PARAMETER	SYMBOL	TEST CONDITIONS	DG304-DG307A DG304A-DG307AA			DG304-DG307B/C DG304A-DG307AB/C			UNITS
				MIN	TYP	MAX	MIN	TYP	MAX	
SWITCH	Analog Signal Range	$V_{ANALOG}$	$I_S = 10mA$ , $V_{in} = 3.5V$ or $11.0V$	-15	15	-15	15	15	V	
	Drain-Source ON Resistance	$r_{DS(on)}$	$V_{in} = 3.5V$ or $V_{in} = 11.0V$	$I_S = -10mA$ , $V_D = 10V$	75	75	75	75	$\Omega$	
	Source OFF Leakage Current	$I_{S(off)}$		$I_S = 10mA$ , $V_D = -10V$	75	75	75	75	$\Omega$	
	Drain OFF Leakage Current	$I_{D(off)}$	$V_S = 14V$ , $V_D = -14V$	$V_S = 14V$ , $V_D = 14V$	100	100	100	100	$nA$	
	Drain ON Leakage Current	$I_{D(on)}$		$V_S = -14V$ , $V_D = 14V$	-100	-100	-100	-100	$nA$	
	Input Current/Voltage High	$I_{INH}$	$V_{in} = 5.0V$	$V_{in} = 5.0V$	-1	-10	-1	-10	$\mu A$	
	Input Current/Voltage Low	$I_{INL}$		$V_{in} = 15V$	1	10	1	10	$\mu A$	
	Positive Supply Current	$I^+$	$V_{in} = 15.0V$ (All Inputs)		100	200	100	200	$\mu A$	
	Negative Supply Current	$I^-$			-100	-200	-100	-200	$\mu A$	
	Positive Supply Current	$I^+$	$V_{in} = 0V$ (All Inputs)		100	200	100	200	$\mu A$	
	Negative Supply Current	$I^-$			-100	-200	-100	-200	$\mu A$	

**Note 1:** Signals on  $S_x$ ,  $D_x$ , or  $IN_x$  exceeding  $V^+$  or  $V^-$  will be clamped by internal diodes. Limit diode forward current to maximum current ratings.

**Note 2:** The algebraic convention whereby the most negative value is a minimum, and the most positive value is a maximum is used in this data sheet.

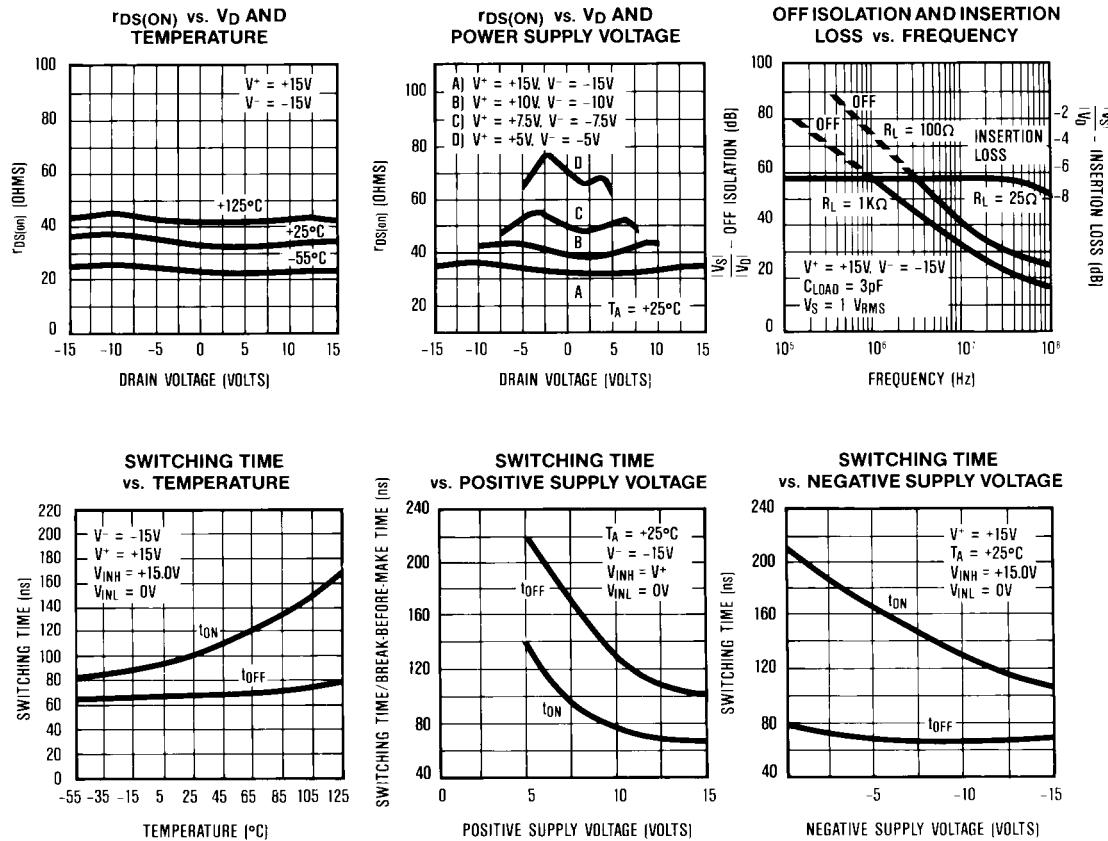
**Note 3:** Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing.

**Note 4:** OFF isolation =  $20 \log \frac{V_S}{V_D}$ ,  $V_S$  = input to OFF switch,  $V_D$  = Output.

# DG304(A)/DG305(A)/DG306(A)/DG307(A)

## CMOS Analog Switches

### Typical Operating Characteristics



### Test Circuits

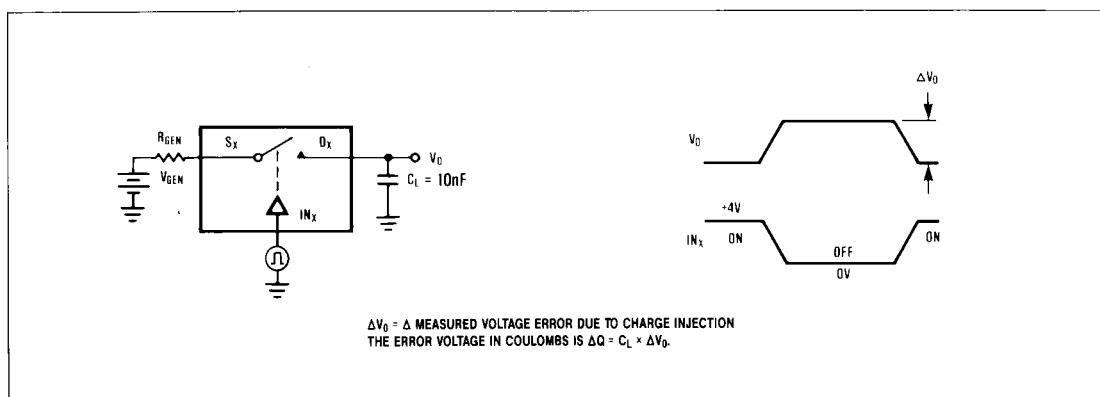


Figure 1. Charge Injection Test Circuit.

## CMOS Analog Switches

### Test Circuits (Continued)

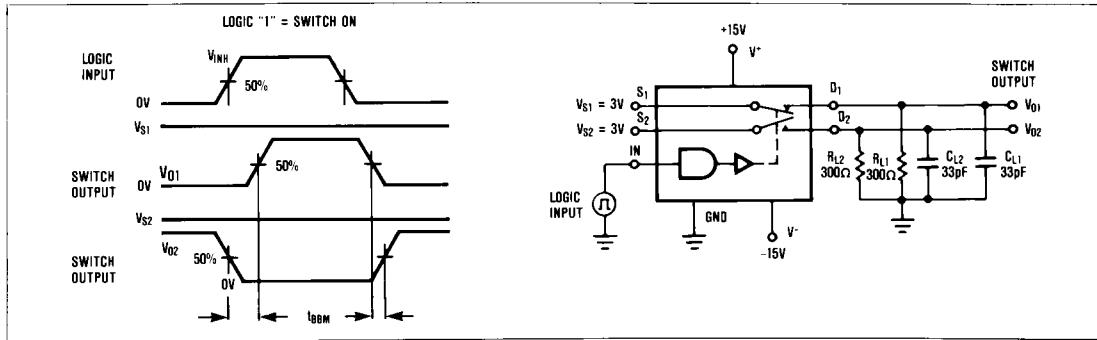


Figure 2. Break-Before-Make Time Test Circuit SPDT DG305(A), DG307(A).

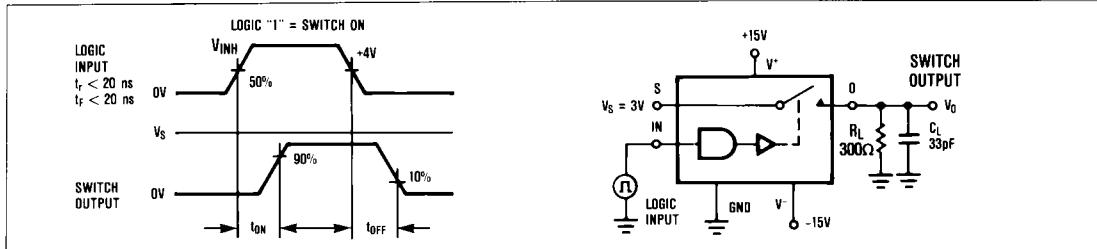


Figure 3. Switching Time Test Circuit.

All DG304 family switches will operate with  $\pm 5$  to  $\pm 15$  V power supplies. They can also be used with single ended power supplies ranging from +10V to +30V where the V<sup>-</sup> terminal is connected to ground. In either case analog signals ranging from V<sup>+</sup> to V<sup>-</sup> can be switched.

Table 1. Typical Single Supply Parameters

	V <sup>+</sup> SUPPLY VOLTAGE (V <sup>-</sup> = 0V)		
	+10V	+15V	+20V
Switching Time (R <sub>L</sub> = 1kΩ)			
t <sub>ON</sub>	220ns	180ns	165ns
t <sub>OFF</sub>	60ns	40ns	30ns
On Resistance			
V <sub>SIGNAL</sub> = +1V	71Ω	51Ω	42Ω
V <sub>SIGNAL</sub> = V <sup>+</sup> /2	77Ω	54Ω	43Ω
V <sub>SIGNAL</sub> = V <sup>+</sup>	84Ω	63Ω	54Ω
Input Logic Levels	3.5V, 11.0V	3.5V, 11.0V	3.5V, 12.5V
			3.5V, 22.0V

The charge injection test circuit is shown in Figure 1. Table 2 lists the typical injected charge for DG304 series switches with various input voltages.

The on resistance variation with analog signal and supply voltage is shown in the Typical Operating Characteristics graphs. The temperature coefficient of R<sub>ON</sub> is typically 0.5%/°C. Typical on resistance matching from channel to channel is 10%. In addition, Table 1 outlines some typical parameters for single supply operation.

### Application Information

Table 2. Charge Injection ( $\pm 15$  V Supplies)

ANALOG INPUT	INJECTED Q
+10V	4pC
+5V	8pC
0V	12pC
-5V	8pC
-10V	5pC

## **CMOS Analog Switches**

### ***Ordering Information (continued)***

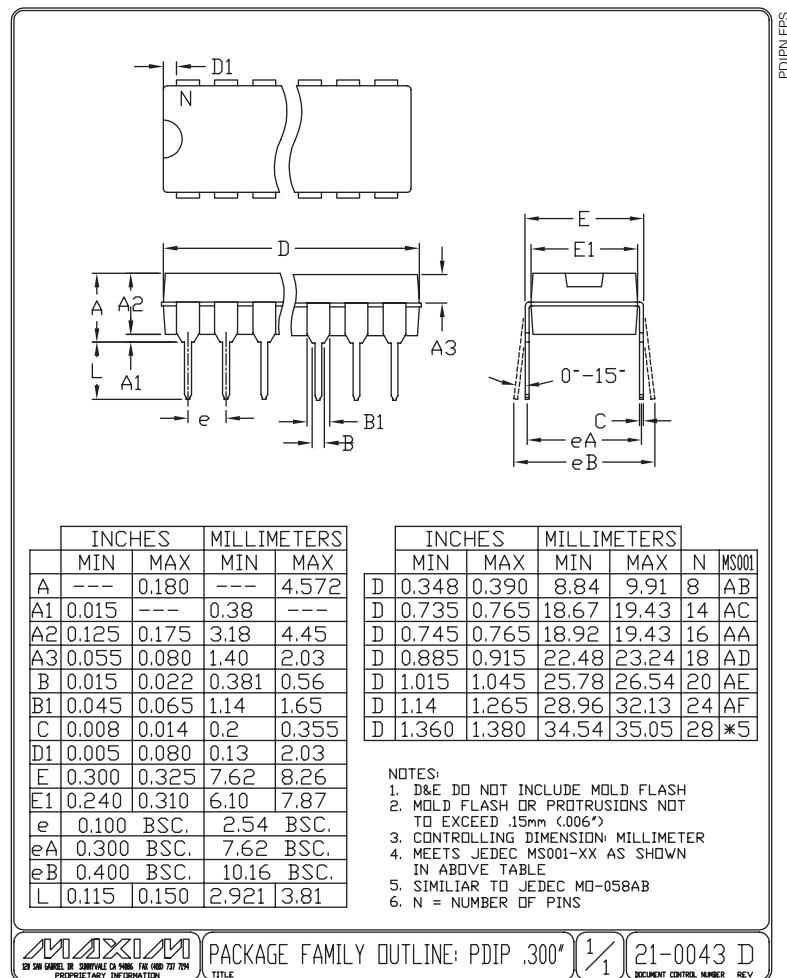
PART	TEMP. RANGE	PACKAGE
DG304AC/D	0°C to +70°C	Dice
DG304ACJ	0°C to +70°C	14 Lead Plastic DIP
DG304ACWE	0°C to +70°C	16 Lead Wide SO
DG304ACK	0°C to +70°C	14 Lead CERDIP
DG304ABWE	-25°C to +85°C	16 Lead Wide SO
DG304ABK	-25°C to +85°C	14 Lead CERDIP
DG304ABA	-25°C to +85°C	10 Lead Metal Can
DG305C/D	0°C to +70°C	Dice
DG305CJ	0°C to +70°C	14 Lead Plastic DIP
DG305CWE	0°C to +70°C	16 Lead Wide SO
DG305CK	0°C to +70°C	14 Lead CERDIP
DG305BWE	-25°C to +85°C	16 Lead Wide SO
DG305BK	-25°C to +85°C	14 Lead CERDIP
DG305BA	-25°C to +85°C	10 Lead Metal Can
DG305AK	-55°C to +125°C	14 Lead CERDIP
DG305AA	-55°C to +125°C	10 Lead Metal Can
DG305AC/D	0°C to +70°C	Dice
DG305ACJ	0°C to +70°C	14 Lead Plastic DIP
DG305ACWE	0°C to +70°C	16 Lead Wide SO
DG305ACK	0°C to +70°C	14 Lead CERDIP
DG305ABWE	-25°C to +85°C	16 Lead Wide SO
DG305ABK	-25°C to +85°C	14 Lead CERDIP
DG305ABA	-25°C to +85°C	10 Lead Metal Can

PART	TEMP. RANGE	PACKAGE
DG306C/D	0°C to +70°C	Dice
DG306CJ	0°C to +70°C	14 Lead Plastic DIP
DG306CWE	0°C to +70°C	16 Lead Wide SO
DG306CK	0°C to +70°C	14 Lead CERDIP
DG306BWE	-25°C to +85°C	16 Lead Wide SO
DG306BK	-25°C to +85°C	14 Lead CERDIP
DG306AK	-55°C to +125°C	14 Lead CERDIP
DG306AC/D	0°C to +70°C	Dice
DG306ACJ	0°C to +70°C	14 Lead Plastic DIP
DG306ACWE	0°C to +70°C	16 Lead Wide SO
DG306ACK	0°C to +70°C	14 Lead CERDIP
DG306ABWE	-25°C to +85°C	16 Lead Wide SO
DG306ABK	-25°C to +85°C	14 Lead CERDIP
DG307C/D	0°C to +70°C	Dice
DG307CJ	0°C to +70°C	14 Lead Plastic DIP
DG307CWE	0°C to +70°C	16 Lead Wide SO
DG307CK	0°C to +70°C	14 Lead CERDIP
DG307BWE	-25°C to +85°C	16 Lead Wide SO
DG307BK	-25°C to +85°C	14 Lead CERDIP
DG307AK	-55°C to +125°C	14 Lead CERDIP
DG307AC/D	0°C to +70°C	Dice
DG307ACJ	0°C to +70°C	14 Lead Plastic DIP
DG307ACWE	0°C to +70°C	16 Lead Wide SO
DG307ACK	0°C to +70°C	14 Lead CERDIP
DG307ABWE	-25°C to +85°C	16 Lead Wide SO
DG307ABK	-25°C to +85°C	14 Lead CERDIP

# CMOS Analog Switches

## Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to [www.maxim-ic.com/packages](http://www.maxim-ic.com/packages).)

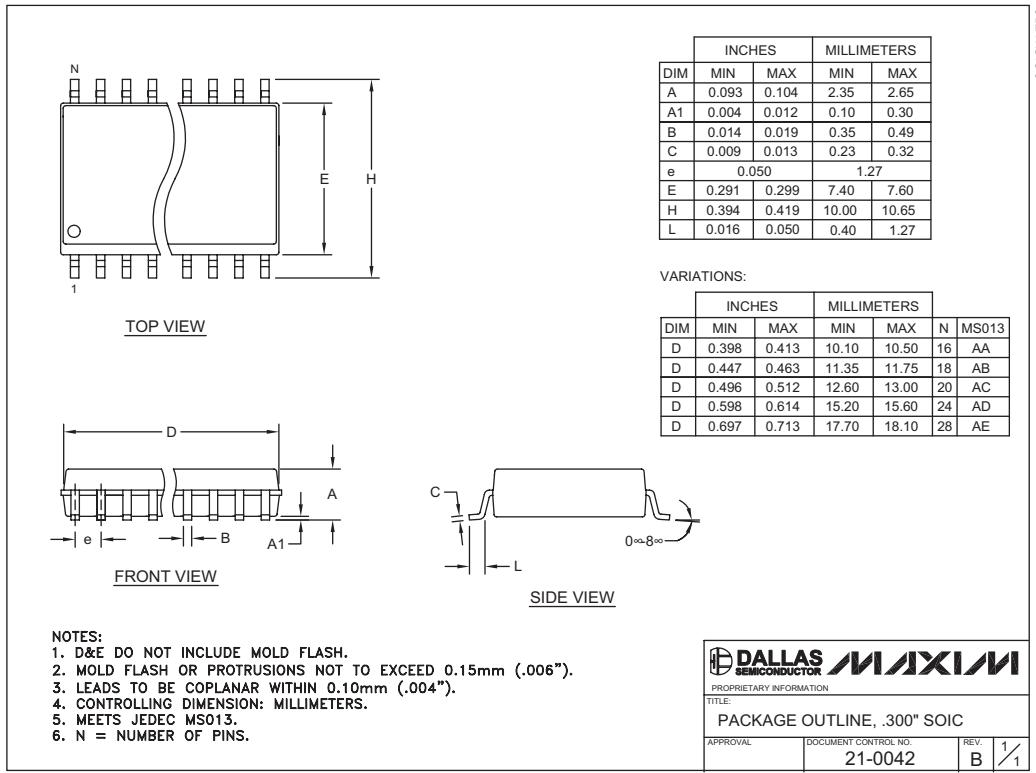


**MAX304(A)/DG305(A)/DG306(A)/DG307(A)**

## CMOS Analog Switches

### Package Information (continued)

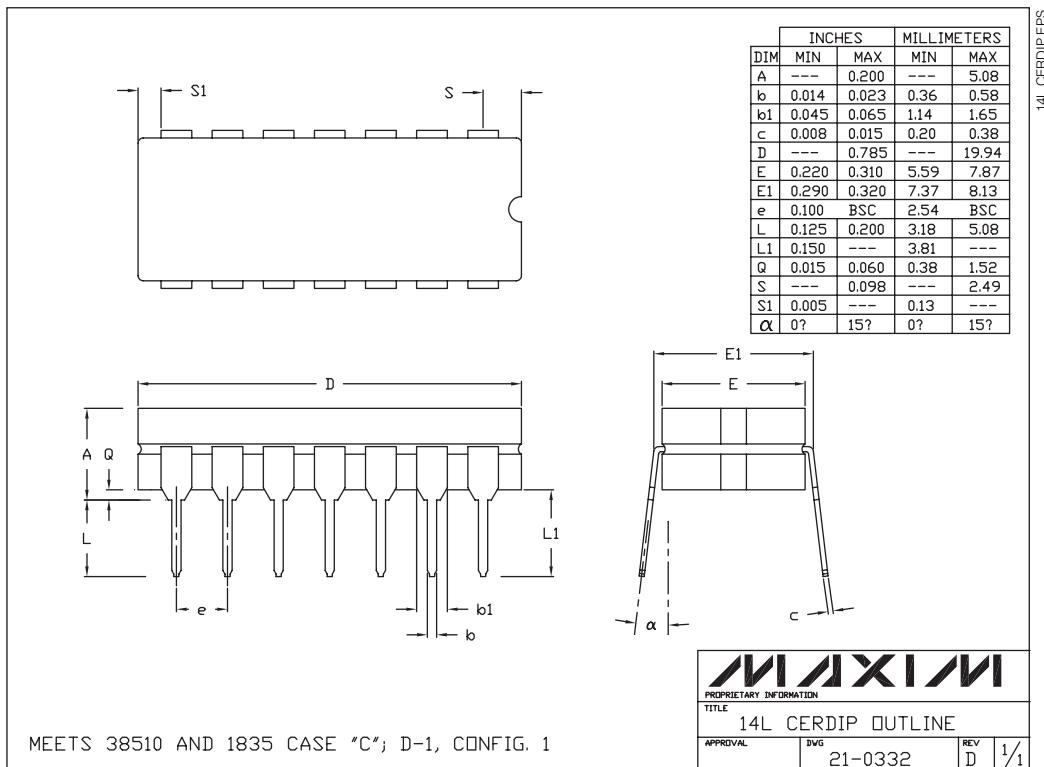
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## **CMOS Analog Switches**

### **Package Information (continued)**

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